

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,836,421 B2  
DATED : December 28, 2004  
INVENTOR(S) : Darrell Rinerson et al.

Page 1 of 1

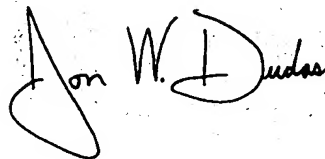
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5.

Lines 58-61, replace "The additional materials might include a non-ohmic device, as is described in co-pending application "High Density NVRAM," U.S. application Ser. No. 10/360,005, filed Feb. 7, 2003, already incorporated by reference." with -- The additional materials might include a non-ohmic device. --.

Signed and Sealed this

Eleventh Day of April, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, rectangular grid pattern.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*